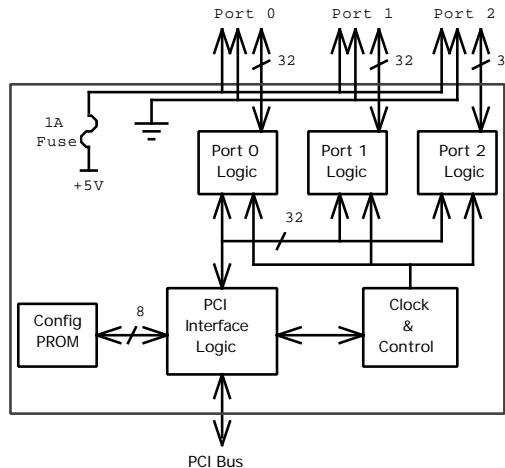




fishcamp engineering

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Features:

- 96 digital input/output signals arranged as three 32-bit ports.
- Independent data direction control on each byte lane.
- Standard 50-pin ribbon cable headers for customer I/O
- 4.25" X 7" card fully compliant with PCI Revision 2.0
- On-board 32-bit wide FIFOs for no-wait state transfers over PCI bus
- Standard TTL logic levels with 24mA drive current on all signal lines
- Includes driver software and complete manual with example applications
- One year warranty

The FPCI-DIO card is a parallel digital interface for computing platforms supporting the PCI expansion bus. The card allows the user to interface a host computer with a wide range of devices such as printers, plotters, BDC-compatible test equipment or even other computers. It provides for 96 TTL compatible signal lines which may be configured, in groups of eight, to act as either input or output signals. Interconnection between the user's circuitry and the FPCI-DIO card is via three standard 50-pin ribbon cable headers on the card.

The FPCI-DIO card is compatible with Rev 2.0 of the PCI local bus specification. The PCI implementation supports a +5 Volt only bus interface in a short card form factor. A full 32 bit interface as well as on board FIFOs enables zero wait-state burst mode operation over the bus. Software drivers supporting various host platforms are available.

All fishcamp engineering products are quality manufactured in the USA and come with a comprehensive set of user documentation including full board level schematics and example application source code. The people at fishcamp engineering are dedicated to bringing you the highest quality products at a competitive price.

FPCI-DIO

PCI Digital I/O Interface



FPCI-DIO Specifications

General:

Card form factor 4.25" X 7" PCI card
 Operating temperature 0 to 70 degrees Celsius
 Storage temperature -55 to 150 degrees Celsius
 Operating humidity 5% to 90%, non-condensing

Platforms supported:

Macintosh YES
 Windows 95 Soon

PCI Local Bus:

PCI Bus vendor ID 1230₁₆
 PCI Bus Device ID 0001₁₆
 Transfer Types Slave Only
 Local Bus FIFO depth 16 long-words
 I/O port data path width 32 bits
 Expansion ROM bus width 8 bits
 Byte Lane Alignment Little Endian

Power Requirement:

Supply Voltage +5 VDC (±5%)
 Current Consumption 870 mA

Customer I/O:

Number of channels 96 Total signals organized into 3 - 32 bit ports
 Data direction control Independent direction control for each byte lane
 Data transfer Programmed I/O
 Maximum data transfer rate 9 Mbytes/sec
 Interface connectors 50 pin ribbon cable headers with .1" pin centers
 Power Available @ I/O Connector +5 VDC (±10%) fused @ 1 A
 +5 volt fuse trip current 2.2 Amps
 +5 volt fuse hold current 1.1 Amps
 Logic Levels TTL

Level	Minimum	Typical	Maximum
Input low voltage	-0.3 V	-	+0.8 V
Input high voltage	+2.0 V	-	+5.5 V
Output low voltage (I _{out} = 12 mA) (I _{out} = 24 mA)	- -	0.25 0.35	+0.4 V +0.5 V
Output high voltage (I _{out} = -3.0 mA) (I _{out} = -15 mA)	+2.4 V +2.0 V	3.2	- -